

# CMS Tracker LVDSBUF (REV.D - TP)

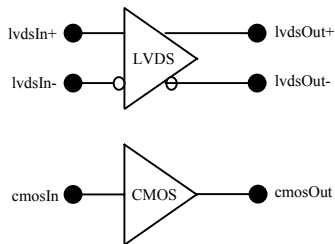
## Description:

The CMS Tracker LVDS BUF contains one LVDS buffer and one CMOS buffer. The LVDS buffer is based on a fully differential structure. The CMOS buffer output is Slew Rate Controlled. The typical rise time on a capacitive load of 10pF is 3ns.

## Features

- 0.25 $\mu$ m CMOS (2.5V)
- Package: SSOP-16 (4.9 x 6.00mm)
- Fully differential LVDS Buffer
- Slew Rate Controlled CMOS Buffer

## Block diagram



## Package and pinout

