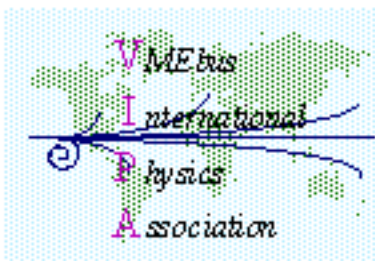


Keying

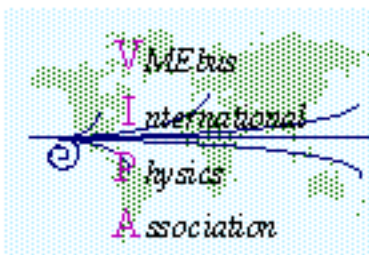
Jan Buytaert

Purpose



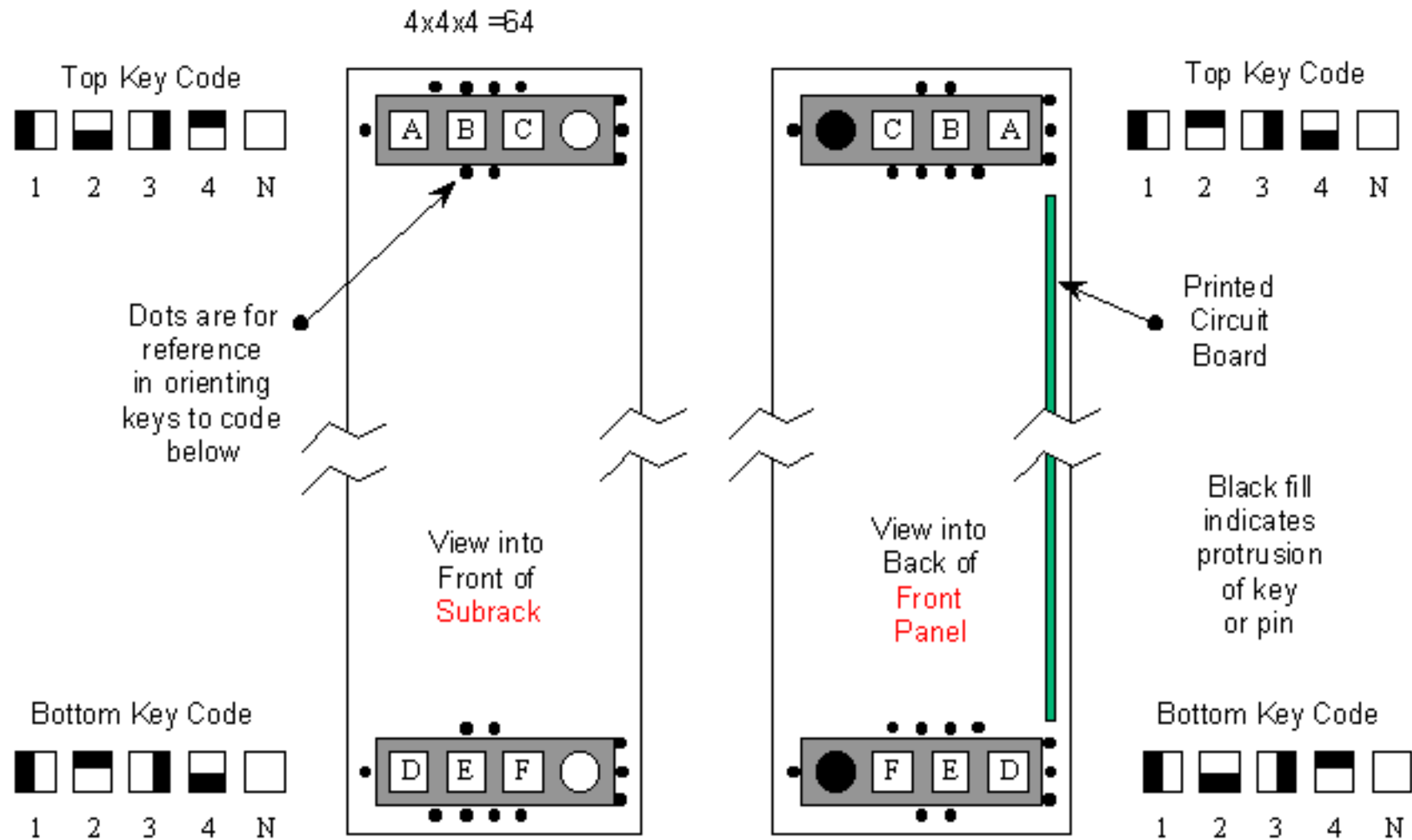
- Protect against damage (mechanical, electrical)
- Guarantees correct operational environment (cooling buses, I/O, ...)
- Eases installation / in-field maintenance

VME64xP

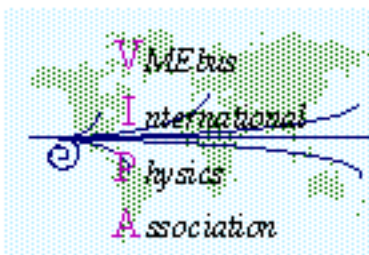


- Appendix C of VME64xP
- Keying is **mandatory** (receptacles on boards and subracks must be installed).
- Choice of the keying scheme is up to the **user**
 - Single universal scheme cannot be achieved, too big a variety of options in VME
- VME64xP lists items (30+) that can require keying

Mechanics

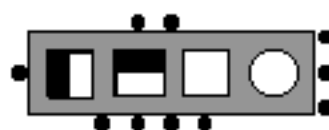


Keying Example



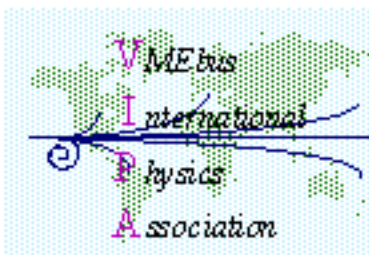
- Example '423/12N'.
- (Front View in subrack)

Top Key Code



bottom Key Code

Hints



- Apply same key for front and corresponding rear slots
- Assign responsibility to a single person (system integrator)
- In one experiments variety boards < 4096 - so randomly assigned key codes will give full protection, guarantee operation and in-field support